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(WO/2002/075650) HARDWARE DESIGN USING EVOLUTION ALGORITHMS

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Applicants:

MARCONI UK INTELLECTUAL PROPERTY LTD [GB/GB]; New Century Park, P.O. Box 53, Coventry CV3 1HJ (GB) (AE, AG, AL, AM, AT, AU, AZ, BA, BB, BE, BF, BG, BJ, BR, BY, BZ, CA, CF, CG, CH, CI, CM, CN, CO, CR, CU, CY, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, FR, GA, GB, GD, GE, GH, GM, GN, GQ, GR, GW, HR, HU, ID, IE, IL, IN, IS, IT, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS. LT, LU, LV, MA, MC, MD, MG, MK, ML, MN, MR, MW, MX, MZ, NE, NL, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, SN, SZ, TD, TG, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZM, ZW only).

YAO, Xin [AU/GB]; 22 Yelverton Drive, Edgbaston, Birmingham B15 3NT (GB) (US Only). SCHNIER, Thorsten [DE/GB]; 74 Corisande Road, Selly Oak, Birmingham B29 6RP (GB) (US Only).

Inventors:

YAO, Xin [AU/GB]; 22 Yelverton Drive, Edgbaston, Birmingham B15 3NT (GB).

SCHNIER, Thorsten [DE/GB]; 74 Corisande Road, Selly Oak, Birmingham B29 6RP (GB).

Agents:

COLLIER, Ian, Terry; Marconi Intellectual Property, Marrable House, The Vineyards. Great Baddow,

Chelmsford, Essex CM2 7QS (GB).

CARDUS, Alan; Marconi Intellectual Property, Marrable House, The Vineyards, Great Baddow,

Chelmsford, Essex CM2 7QS (GB).

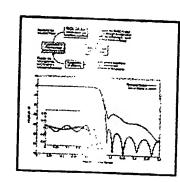
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Title:

HARDWARE DESIGN USING EVOLUTION ALGORITHMS

Abstract:

The design of a hardware component such as a digital filter can be optimised by taking an initial population of filter designs and encoding them as chromosomes. The fitness of each chromosome is then evaluated and parent chromosomes are then selected based on the fitness criteria. Offspring chromosomes are then generated ussing genetic operations such as mutation and crossover from the pool of offspring, and optionally parents, individuals are selected to survive using a combination of Pareto fronts based on non-dominated individuals and clustering. The process is repeated or until a termination criteria is satisfied.



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AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU. SD, SE, SG, SI, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZM, ZW.

African Regional Intellectual Property Org. (ARIPO) (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG,

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